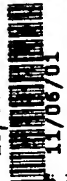


1996 U.S. PTO
10/008389

JS 10/9



U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10008389	FILING DATE 11/06/2001	CLASS 438	SUBCLASS 1.2	GAU 2812	EXAMINER
**APPLICANTS: Oda Nobuhiko; Yamaji Toshifumi; Nakanishi Shiro; Morimoto Yoshihiro; Yoneda Kiyoshi;					
**CONTINUING DATA VERIFIED: <i>16</i> <i>17</i>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-338708 11/07/2000 <i>yes</i> <i>17</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		YKI-0079			
TITLE : Bottom gate-type thin-film transistor and method for manufacturing the same					
U.S. DEPT. OF COMM/PAT & TM-PTO-438L (Rev. 10-94)					

BEST AVAIL ABLE CO

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Applicati n Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM
(Attached in pocket on right inside flap)